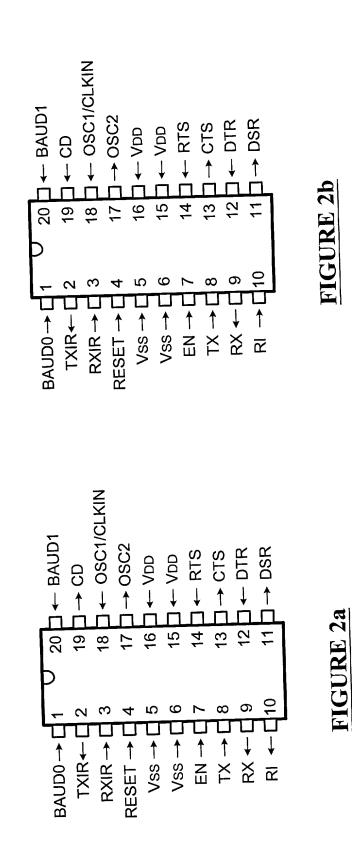
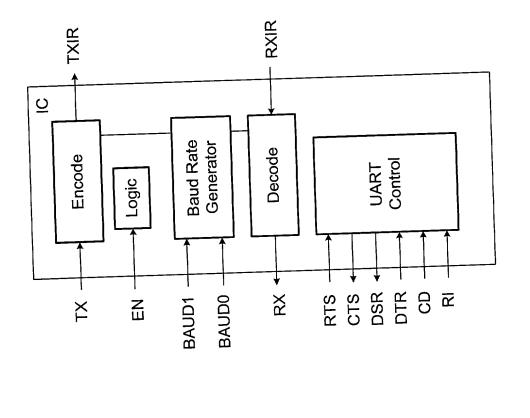


FIGURE 1b

FIGURE 1a





RXIR TXIR $\underline{\circ}$ Baud Rate Generator Decode Encode UART Control Logic CD BAUD1 DSR DTR $\overline{\mathbf{x}}$ $\overset{\sim}{\sim}$ CTS RTS **BAUD0** N N ×

FIGURE 3a

FIGURE 3b

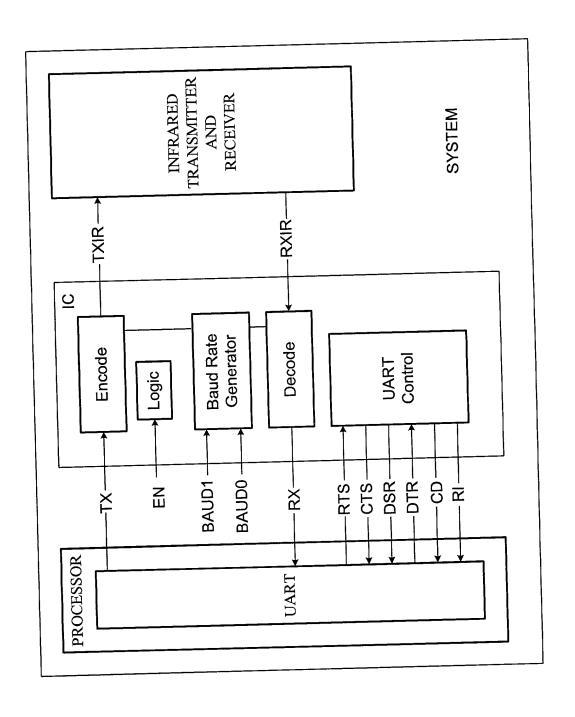


FIGURE 4a

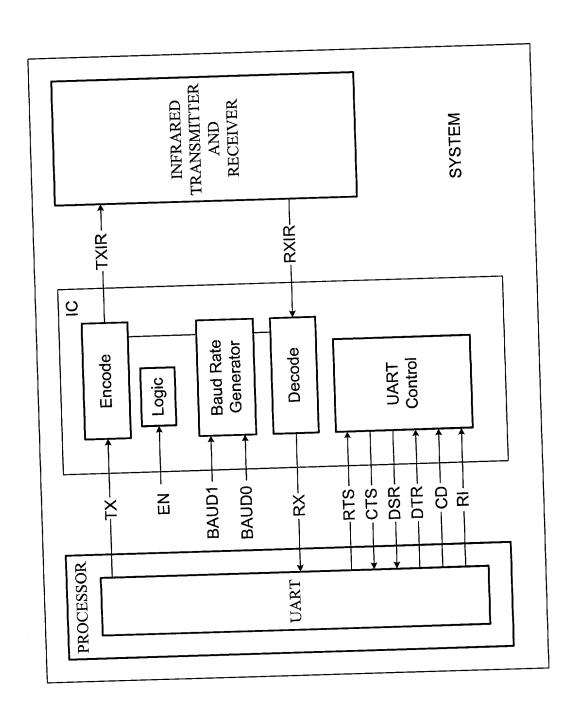


FIGURE 4b